

11
12-13-02
Mullish



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application Serial No. 10/050,348
Filing Date January 15, 2002
Inventor Gurtej S. Sandhu et al.
Assignee Micron Technology, Inc.
Group Art Unit 2813
Examiner Schillinger, Laura M.
Attorney's Docket No. MI22-1898
Title: Transistor Structures

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

To: Assistant Commissioner for Patents
Washington, D.C. 20231

From: David G. Latwesen, Ph.D. (Tel. 509-624-4276; Fax 509-838-3424)
Wells St. John P.S.
601 W. First Avenue, Suite 1300
Spokane, WA 99201-3828

RECEIVED
DEC 11 2002
TC 2800 MAIL ROOM

Dear Sir:

The Examiner's attention is directed to the references which are listed on the attached Form PTO-1449 with copies of non-patent literature attached.

Citation of these references is respectfully requested.

A check in the amount of \$180.00 is enclosed to cover the fee specified under 37 C.F.R. § 1.17(p).

Respectfully submitted,

Dated:

12/5/02

By:

David G. Latwesen, Ph.D.
Reg. No. 38,533

12/10/2002 NMOHAMM1 00000080 10050348

01 FC:1806

180.00 DP

EV182657085